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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPLICATION NUMBER	FILING DATE	CLASS	SUBCLASS	GROUP ART UNIT	EXAMINER
APPL NUM 10062942	FILING DATE 01/31/2002	CLASS 431	SUBCLASS 257	GROUP ART UNIT 2811	EXAMINER KOR. SIA

**APPLICANTS: Vasquez Barbara; Wallis David; Winter Sylvia;

**CONTINUING DATA VERIFIED: NO AR 10/1/02

** FOREIGN APPLICATIONS VERIFIED: NO 10/1/02

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	P2002,0075
Verified and Acknowledged Examiners's initials AR 10/1/02		
TITLE : Method of applying a bottom surface protective coating to a wafer, and wafer dicing method		

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Applicant's Examiner	
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